


## Contents

[Page 01].	Block Diagram
[Page 02].	Power Tree
[Page 03].	Revision History
[Page 04].	Main SR110
[Page 05].	QSPI, PSRAM
[Page 06].	CSI_RX/TX, DVP
[Page 07].	M.2_KeyE (WiFi/BT)
[Page 08].	IMU, ALS
[Page 09].	GPIO Expander
[Page 10].	Pin Headers, JTAG, DMIC
[Page 11].	Debug IC
[Page 12].	POWER-USB, Battery
[Page 13].	RTC, Wake-up

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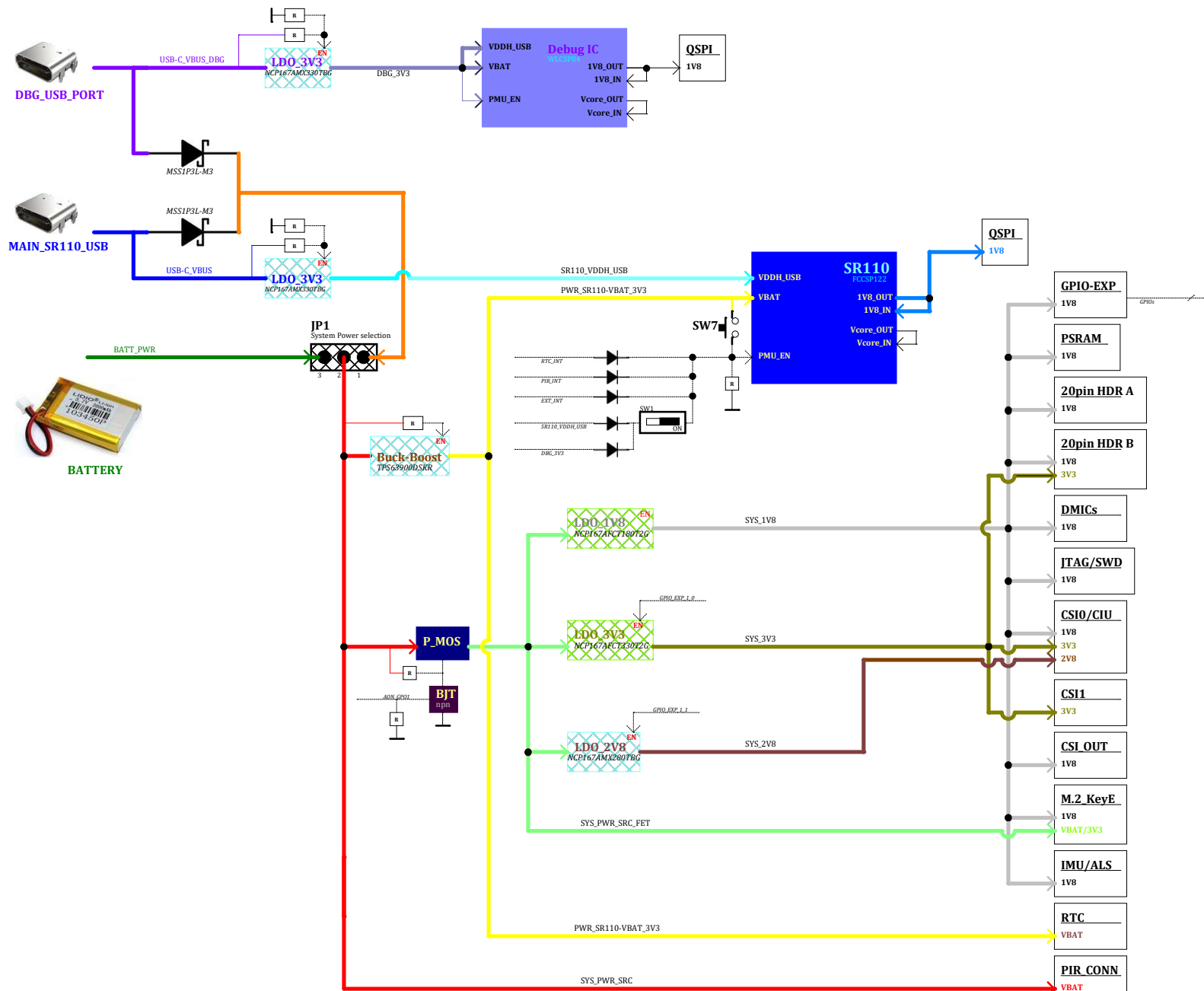
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File			
01 : BLOBK DIAGRAM			
Size	Document Number	Rev	
	SC950-C01116-01	E	
Date:	Wednesday, September 03, 2025	Sheet	1 of 13

# Power Tree



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File	02 : POWER TREE	Rev	E
Doc Number	SC950-C01116-01		
Date	Wednesday, September 03, 2025	Sheet	2 of 13


Revision History

Rev	Date	Originator	Change Detail	Reason	SW Impact
A	10/31, 2024	Iris Fan	Initial Release.		
B	02/25, 2025	Iris Fan	1. Change D19, D20 parts to VISHAY low Ir part MSS1P3L-M3. [Page.12] 2. Change R208 to 27kohm and default stuffed. [Page.12] 3. Add 4.7k pull down R1941 to U23 ctrl. [Page.12] 4. Add R1945 pull down resister to U22. [Page.12]	[#1~4] Reduce Leakage from Schottky to System LDOs.	NO
			5. Change R1894 value to 100 ohm and change its pull up power to PWR_SR110-VBAT_3V3 [Page.5] 6. Move D30 Blue LED to U28 output and remove Q73. [Page.12] 7. Modify chip name. SR110_FCCSP122 for U2, SR110_WLCSP84 for U19. [Page.4, 11]	[#5] provide enough Vf to turn on LED of SPI access indicator. [#6] Make Blue LED ON only when main SR110 USB is powered. [#7] Add package info to tell two MCUs apart.	NO NO NO
			8. Stuff U36 (Buck-Boost) and L4 as default. Remove U37 (LDO). [Page.12] 9. Remove U1 (32.768KHz supply). [Page.4] 10. Fix J23 pin32 power to SYS_3V3. [Page.6] 11. Remove SW6. [Page.11] 12. Remove GPIO41 CLKOUT option to J21. [Page.6] 13. Remove SR110_RSTn option and R1863 to J27. [Page.10] 14. Remove CSI_OUT_GPIO control (J2 pin12) for SW8. [Page.6]	[#8~14] Remove unused X'tal, power option, control GPIOs and switch.	NO
			15. Change R202 to 100k ohm. [Page.12] 16. Cgange R1856 to a strong 1K PD. [Page.12]	[#15~16] Change to stronger PDs.	NO
			17. Add J29 for power measurement. [Page.12] 18. Add TP12, 13 for SR110_VDDIO1P8 and TP14, TP15 for Vcore. [Page.4]	[#17~18] For Power measurement.	NO
			19. Add user button SW8. [Page.9] 20. Change UART1 connection from UART1_A to UART1_B. [Page.11] 21. Change M2-KEYE_W_DISABLE2n control to GPIO43. [Page.7] 22. Route GPIO41 to J24 pin15 and GPIO10 to J25 pin2. and add 0 ohm resistors R1952 and R1953 for each. [Page.6]	[#19~22] Specification Change.	YES
			21. Reserve R1951 for DAP reset. [Page.11] 22. Change R164 to 0603 footprint for easy rework. [Page.11]	[#21~22] For DAP IC development.	NO
C	05/06, 2025	Iris Fan	23. Fix swapped UART0_TX/RX for BT. [Page.4, 7]	Bug fix	NO
			24. Add stuffing options R1960, R1961 and R1968~R1970 to J23 for 2-wire serial camera module. [Page.6]	Support 2-wire serial camera from camera module.	NO
			25. Change P/N for 2.2uF/6.3V caps, Varistors, DMICs U17, U18 and QSPI U20.	Reduce BOM cost	NO
			26. Remove J16 for Debug IC GPIOs. [Page.11]	Save board space to put low cost QSPI	NO
			27. Change DMIC connection to DM1. [Page.10]	Let DVP and DMIC can work simultaneously	YES
			28. Remove I2C1 stuffing options to devices. [Page.6,8,13]	Remove unused option.	YES
			29. DNS U4 (PSRAM) as default. [Page.5]	Reduce BOM cost	YES
			30. Mount R1900 and R1902 with 4.7K-ohm as default. [Page.4, 11]	Insure boot-up state of TESTn set to High.	NO
			31. Change DMIC pins from DM1 to DM0_C (GPIO15,16) [Page.8, 13, 10]	Let DMIC and DVP can work simultaneously and move according GPIO control to others.	YES
			32. Change I2C Devices control from I2C0 to I2C1_B (GPIO42,43) [Page.8, 13, 10]		
			33. Change W_DISABLE2n control to GPIO25. Change DVP sleep control to GPIO28. [Page.6, 7]		
			34. Add R1975 for PWR_M2-KEYE_VIO. [Page.7]	For power measurement	NO
			35. Update U2, U19 Symbol. [Page.4, 11]	To match datasheet ball name	NO
D	06/09, 2025	William W	36. Add R1978, R1979 2.2K pull-up resistor to U7	There is no pull-up resistors on lever1 shifter U7 1.8V side.	NO
			37. Update some alternative parts for ROHS_HF	To match ROHS_HF CERT.	
			38. Remov D5, D8. add D3, D4, D6,D7		
			39. Change U23, U28 from TCR3UM33A to NCP167AMX330TBG		
			40. Change U29 from TCR3UM28A to NCP167AMX280TBG		
			41. Change R187=DNS	Ensure SR110_RST will be normally	
E	08/25, 2025	William W	42. Change J2, J21 part number to HFW15R-1STE1H1LF	To match ROHS_HF CERT.	NO
			43. Change R18 from 47K to 4.7K	Improve SR110_RSTn	

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03 : REVISION HISTORY

Size

Document Number

SC950-C01116-01

Rev

E

Date:

Wednesday, September 03, 2025

Sheet

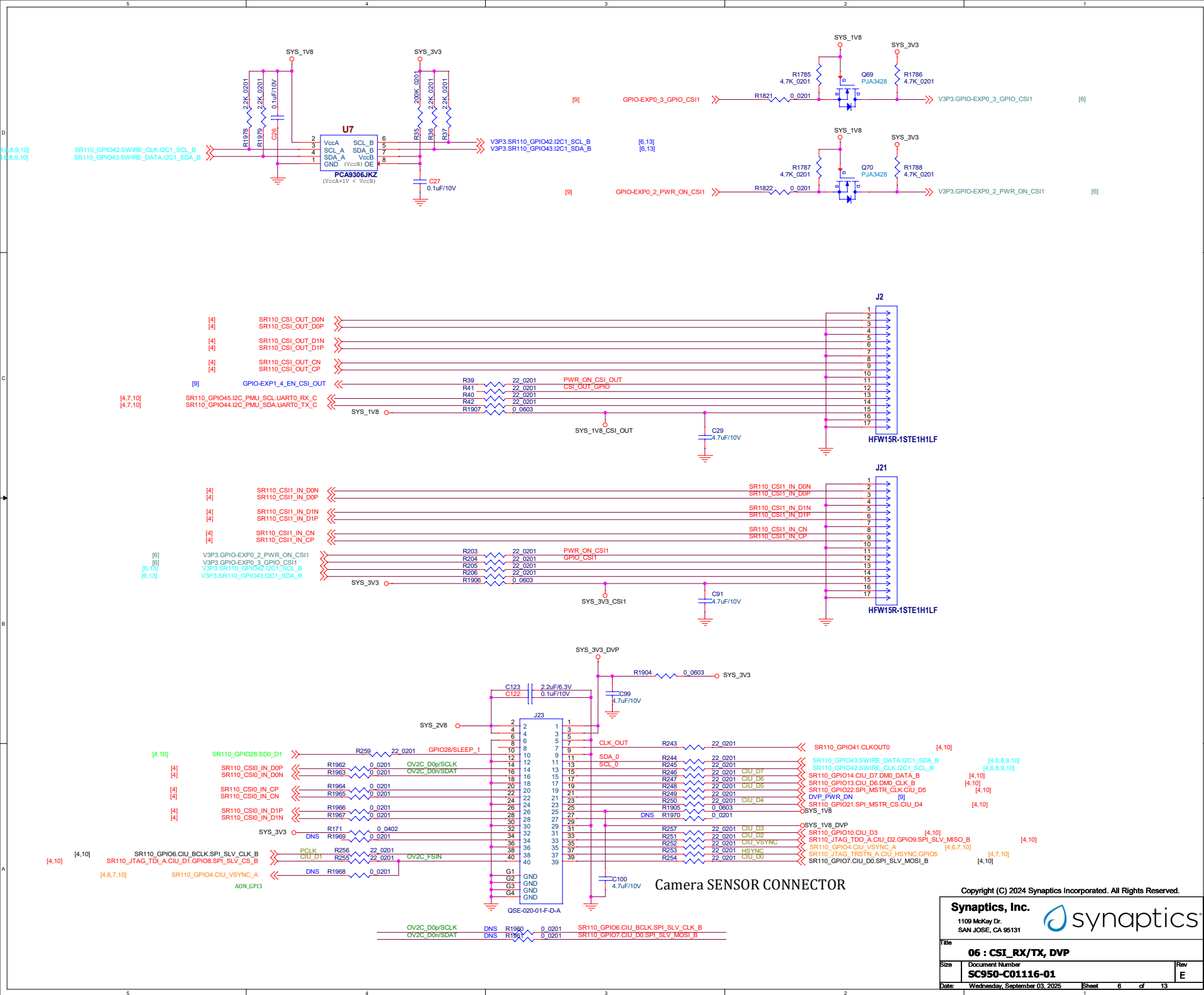
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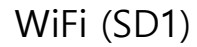
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13

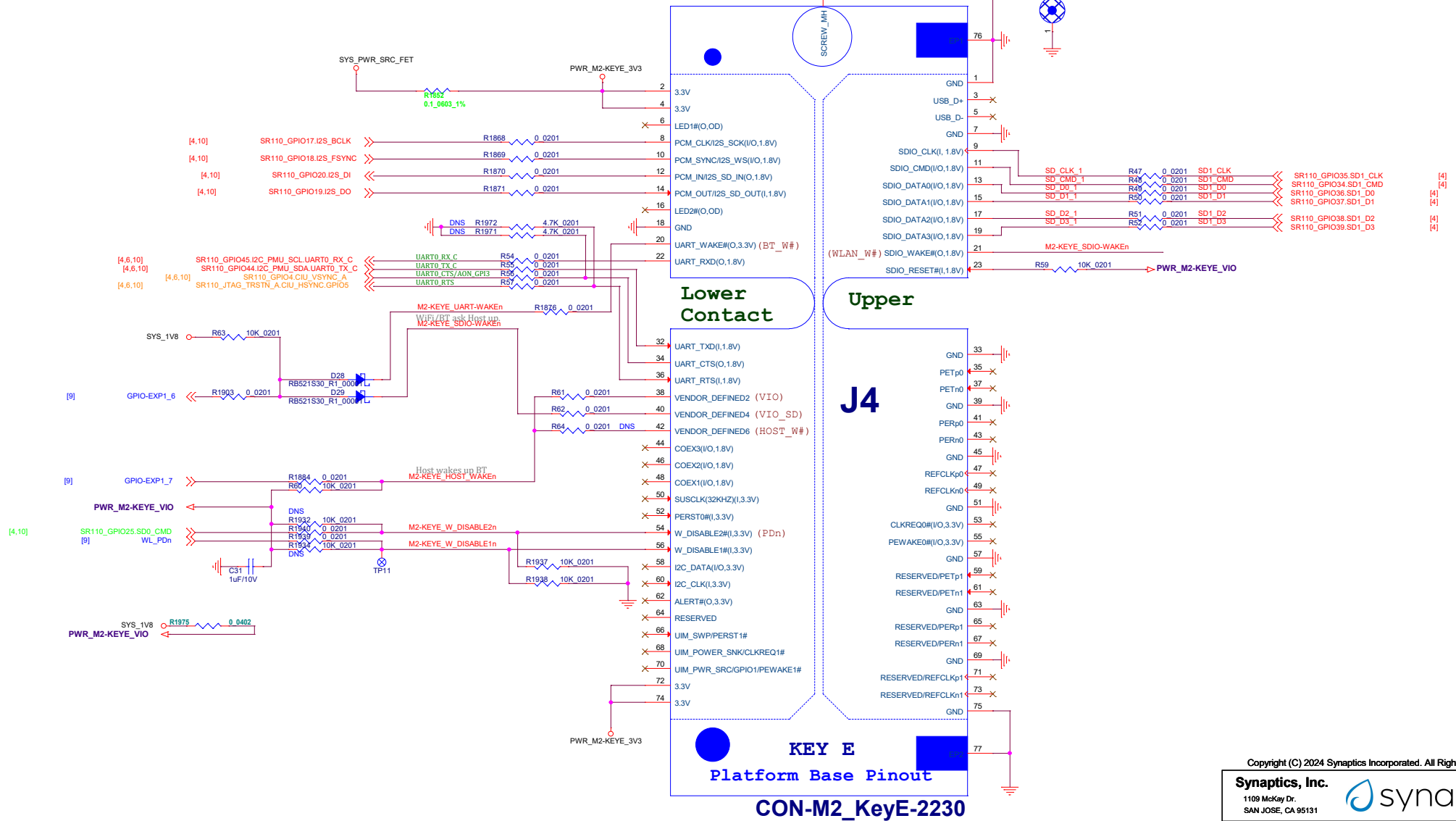








**STO1**  
StandOff



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Title **07 : M.2 KeyE**

Size	Document Number <b>SC950-C01116-01</b>
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Date: Wednesday, September 03, 2025 Sheet 7 of 13

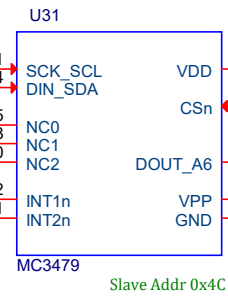
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[4,6,8,9,10]

SR110\_GPIO42.SWIRE\_CLK.I2C1\_SCL\_B  
SR110\_GPIO43.SWIRE\_DATA.I2C1\_SDA\_B

[9]  
[9]

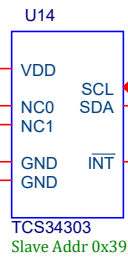
IMU\_INT1n  
IMU\_INT2n

SYS\_1V8



SYS\_1V8

SYS\_1V8



R1843 0 0201

R1929 22 0201

R1928 22 0201

R105 4.7K 0201

SYS\_1V8  
ALS\_INTn

[9]

SR110\_GPIO42.SWIRE\_CLK.I2C1\_SCL\_B  
SR110\_GPIO43.SWIRE\_DATA.I2C1\_SDA\_B

[4,6,8,9,10]  
[4,6,8,9,10]

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08 : IMU, ALS

Size

Document Number

SC950-C01116-01

Rev

E

Date:

Wednesday, September 03, 2025

Sheet

8

of

13





